# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of )
)
    Merchant, et al. )      Examiner:   Hamilton, M.G.
)
Serial No. 09/684,160 )      Art Unit:     2172
)
Filing Date: October 4, 2000 )
)
For:   Method and System for )
       Generating A Bit Order Data )
       Structure Of Configuration Bits )
       From A Schematic Hierarchy )
)

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Technology Center 2100

Hon. Assistant Commissioner for Patents
Washington, D.C. 20231

## AMENDMENT AND RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action mailed September 12, 2002, the following amendments and remarks to the above captioned patent application are respectfully submitted. Reconsideration of the above captioned patent application is respectfully requested.

IN THE SPECIFICATION:

Please replace the paragraph beginning on page 24, line 18 with the following rewritten paragraph: